

What is claimed is:

1. A method comprising:

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identifying a plurality of data fields within the packet; and

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3. The method of claim 2, further comprising:

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5. The method of claim 4, wherein the disk is one of an optical disk and a magnetic disk.

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6. The method of claim 1, wherein the storage device is memory within a computer.

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8. The method of claim 7, wherein the item of test equipment is a logic analyzer.

9. The method of claim 8, wherein the logic analyzer is connected to a bus system.

5 10. The method of claim 7, wherein the item of test equipment reads the plurality of trace vectors in synchronization with a clock signal.

10 11. The method of claim 10, wherein the plurality of trace vectors are read in synchronization with rising edges and falling edges of the clock signal.

12. The method of claim 1, wherein identifying the subset of the trace vectors includes monitoring a flag bit.

13. A computer program product in a computer readable medium, comprising instructions for:

15       reading a plurality of trace vectors from a file on a storage device;  
          identifying a subset of the trace vectors, wherein the subset of the trace vectors forms a packet;  
          identifying a plurality of data fields within the  
20 packet; and  
          presenting each of the data fields as output.

14. The computer program product of claim 13, wherein reading the plurality of trace vectors includes reading the subset of the trace vectors into memory.

25 15. The computer program product of claim 14, comprising additional instructions for:

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reading a second subset of the trace vectors into the memory, wherein the second subset of the trace vectors forms a second packet.

5 16. The computer program product of claim 13, wherein the storage device is a disk.

17. The computer program product of claim 16, wherein the disk is one of an optical disk and a magnetic disk.

18. The computer program product of claim 13, wherein the storage device is memory within a computer.

10 19. The computer program product of claim 13, comprising additional instructions for:  
reading the plurality of trace vectors from an item of test equipment; and  
storing the plurality of trace vectors in the file.

15 20. The computer program product of claim 19, wherein the item of test equipment is a logic analyzer.

21. The computer program product of claim 20, wherein the logic analyzer is connected to a bus system.

20 22. The computer program product of claim 19, wherein the item of test equipment reads the plurality of trace vectors in synchronization with a clock signal.

23. The computer program product of claim 22, wherein the plurality of trace vectors are read in synchronization with rising edges and falling edges of the clock signal.

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24. The computer program product of claim 13, wherein identifying the subset of the trace vectors includes monitoring a flag bit.

25. A data processing system comprising:

- 5       a bus system;  
      a processing unit connected to the bus system and including at least one processor;  
      memory connected to the bus system;  
      a set of instructions stored in the memory,  
10       wherein the processing unit executes the set of instructions to perform the acts of:  
      reading a plurality of trace vectors from a file on a storage device;  
      identifying a subset of the trace vectors, wherein the  
15       subset of the trace vectors forms a packet;  
      identifying a plurality of data fields within the packet; and  
      presenting each of the data fields as output.

26. The data processing system of claim 25, wherein reading  
20       the plurality of trace vectors includes reading the subset of the trace vectors into the memory.

27. The data processing system of claim 26, wherein the processing unit executes the set of instructions to perform the additional acts of:

- 25       reading a second subset of the trace vectors into the memory, wherein the second subset of the trace vectors forms a second packet.

28. The data processing system of claim 25, wherein the storage device is a disk.

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29. The data processing system of claim 28, wherein the disk is one of an optical disk and a magnetic disk.

30. The data processing system of claim 25, wherein the storage device is memory within a computer.

5 31. The data processing system of claim 25, wherein the processing unit executes the set of instructions to perform the additional acts of:

reading the plurality of trace vectors from an item of test equipment; and

10 storing the plurality of trace vectors in the file.

32. The data processing system of claim 31, wherein the item of test equipment is a logic analyzer.

33. The data processing system of claim 32, wherein the logic analyzer is connected to a bus system.

15 34. The data processing system of claim 31, wherein the item of test equipment reads the plurality of trace vectors in synchronization with a clock signal.

20 35. The data processing system of claim 34, wherein the plurality of trace vectors are read in synchronization with rising edges and falling edges of the clock signal.

36. The data processing system of claim 25, wherein identifying the subset of the trace vectors includes monitoring a flag bit.

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